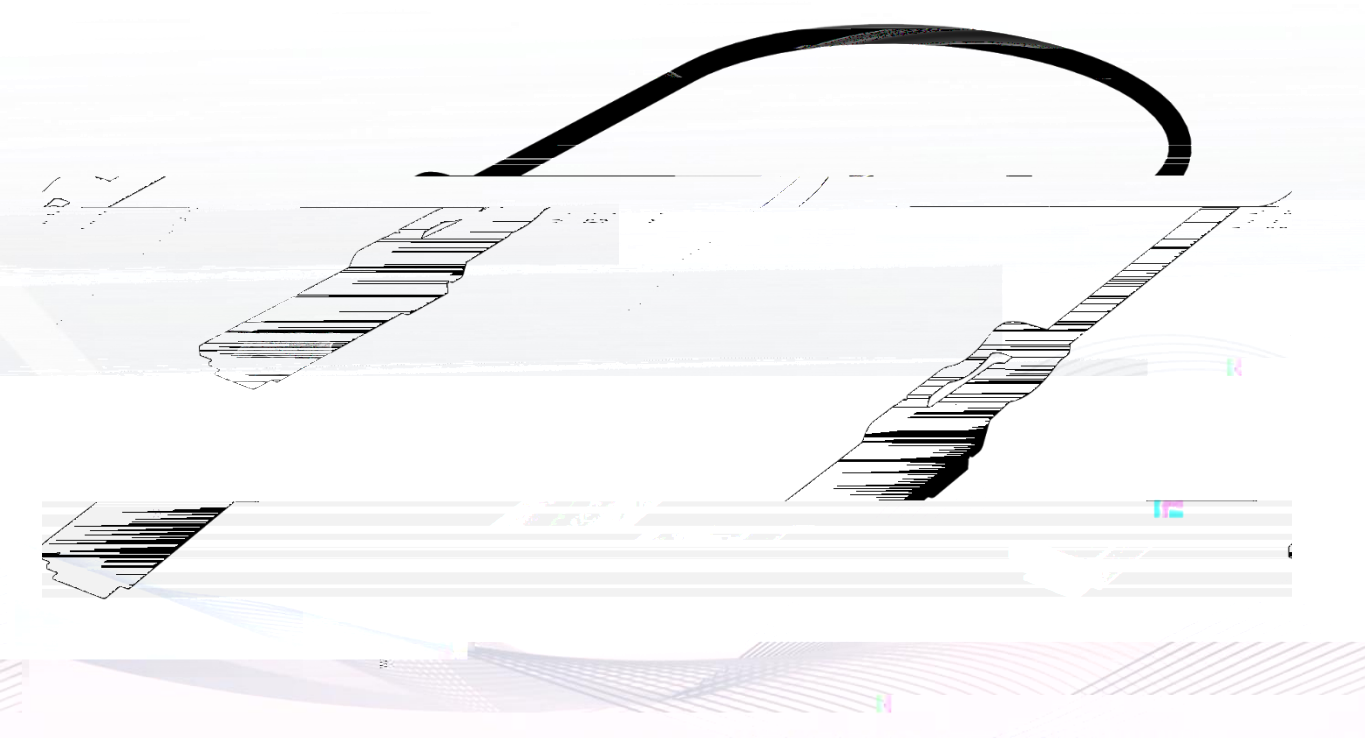

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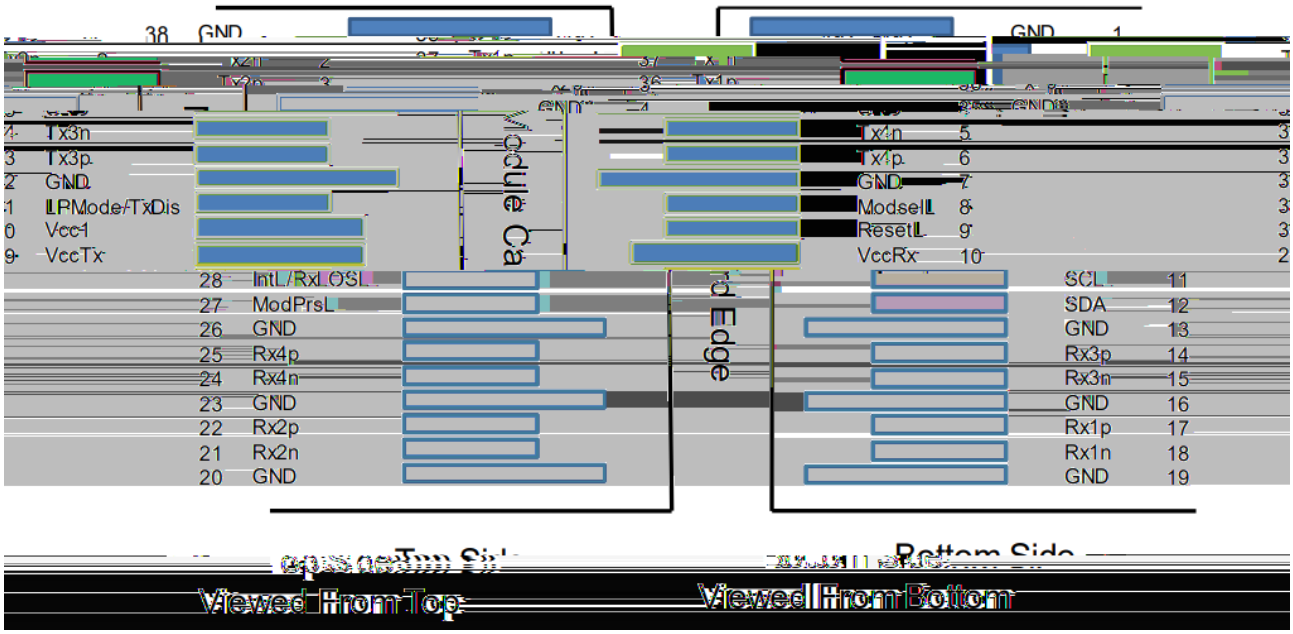
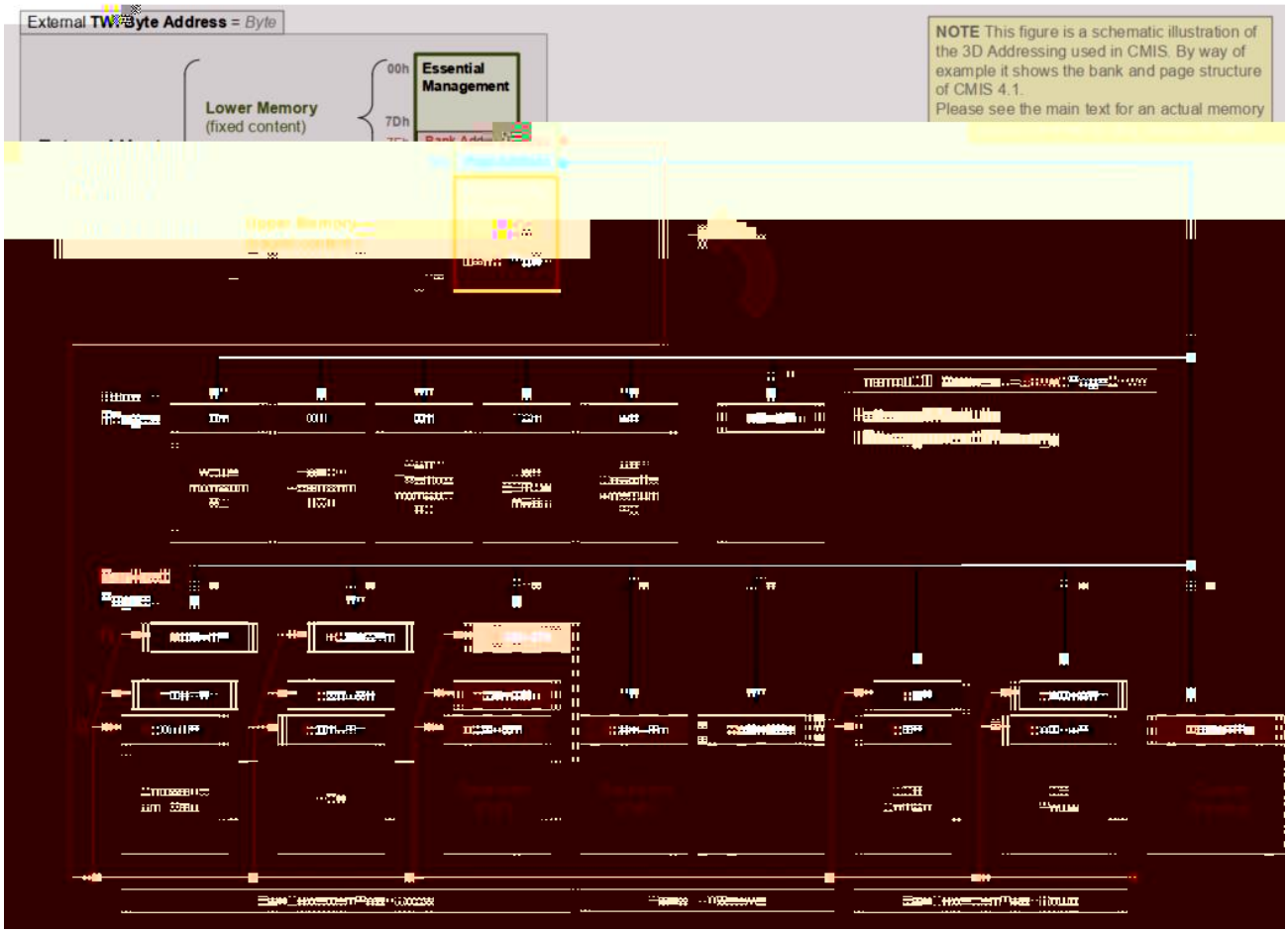

38 GND
Tx3n
Tx3p
GND
LPMode/TxDis
Vcc1
VccTx
28 IntL/RxLOSL
27 ModPrsL
26 GND
25 Rx4p
24 Rx4n
23 GND
22 Rx2p
21 Rx2n
20 GND
Tx4n 5
Tx4p 6
GND 7
ModselL 8
ResetL 9
VccRx 10
SCL 11
SDA 12
GND 13
Rx3p 14
Rx3n 15
GND 16
Rx1p 17
Rx1n 18
GND 19
Viewed From Top
Viewed From Bottom


External TWI Byte Address = Byte
Lower Memory
(fixed content)
00h
Essential
Management
7Dh
NOTE This figure is a schematic illustration of the 3D Addressing used in CMIS. By way of example it shows the bank and page structure of CMIS 4.1.
Please see the main text for an actual memory

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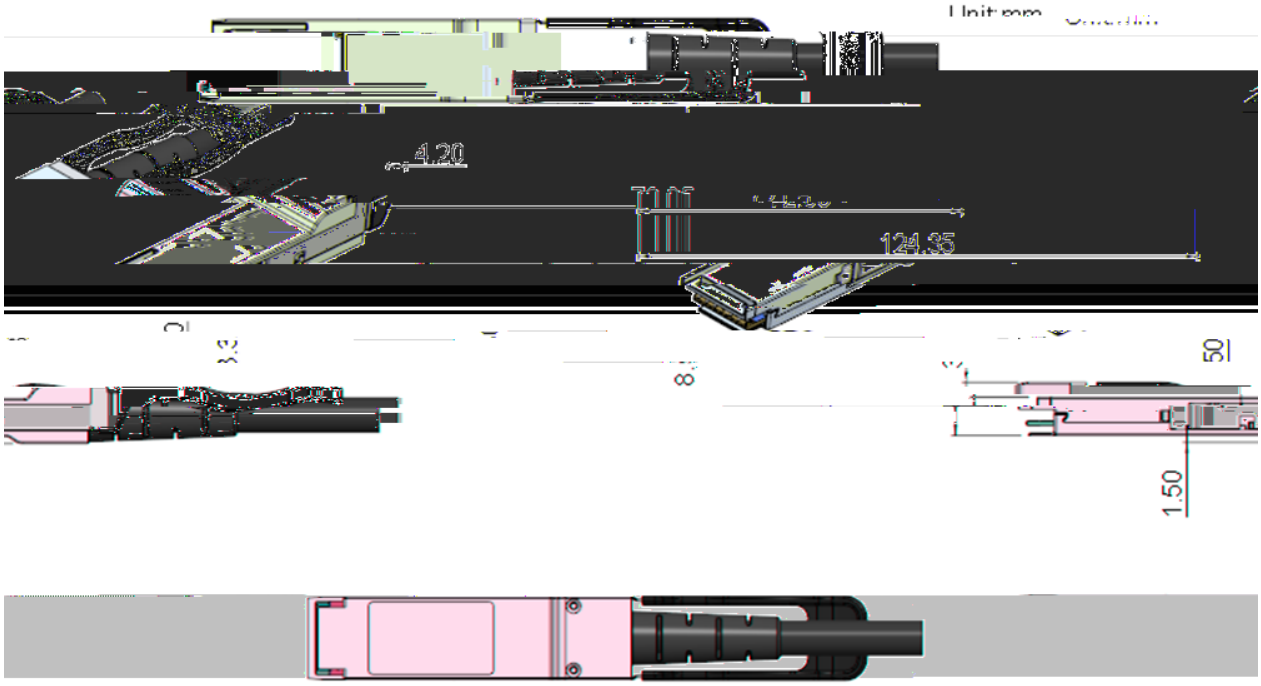
Unit:mm
4.20
124.35
32
8
50
1.50

Unit:mm